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6P2817

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: BAKKER et al. Examiner: Choe, H.
Serial No.: 09/826,571 Group Art Unit: 2817
Filed: April 5, 2001 Docket No.: PHA 80-003A
Title: CIRCUIT, INCLUDING FEEDBACK, FOR REDUCING DC-OFFSET
AND NOISE PRODUCED BY AN AMPLIFIER

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this communication is being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on December 19, 2001.

By: Erin M. Nichols
Erin M. Nichols

OFFICE ACTION RESPONSE AND AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

02/13/2002 BWP:00000003 500996 09826571

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In response to the Office Action dated September 19, 2001, please consider the following amendments and remarks:

In The Claims

Please replace the indicated claims as follows (attached to this Response find marked-up revision to the claims):

4. (Amended) A circuit comprising: an amplifier producing a DC-offset and noise having an input for receiving an input signal from an input of the circuit, and an output for delivering an output signal to an output of the circuit; means for reducing the DC-offset and the noise produced by the amplifier; and feedback means for further reducing the DC-offset produced by the amplifier wherein the further feedback means for further reducing the DC-offset produced by the amplifier comprises switching means for short-circuiting the input signal under control of a start-up signal; and means for adding back a sampled output signal to the amplifier for adapting the DC-offset of the amplifier.